



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2826  
#7  
with  
Formal  
Drawings  
11-14-02  
Roth

Applicant: **Norito Umehara, et al.**  
Serial No.: **09/866,244**  
Filed: **05/25/2001**  
For: **Semiconductor Device and Method of  
Manufacturing Same**

Docket No.: **TIJ-28795**  
Examiner: **TBD**  
Art Unit: **2811**  
Conf. No.: **4574**

RECEIVED  
NOV - 8 2002  
TJ-800 MAIL ROOM

TRANSMITTAL OF FORMAL DRAWINGS

November 1, 2002

Assistant Commissioner For Patents  
Attn: Official Drafts Person  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with  
the U.S. Postal Service as First Class Mail in an envelope addressed  
to: Assistant Commissioner For Patents, Washington, DC 20231.

*Elizabeth Austin*  
Elizabeth Austin  
Date *11/1/2002*

Dear Sir:

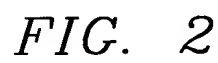
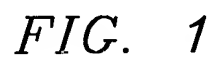
Submitted herewith are 5 sheets of formal drawings.

The enclosed drawings are being provided in a timely manner therefore no additional  
fee is required.

Texas Instruments Incorporated  
P. O. Box 655474, MS 3999  
Dallas, Texas 75265  
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Respectfully submitted,

*Michael K. Skrehot*  
Michael K. Skrehot  
Attorney for Applicants  
Reg. No.: 36,682





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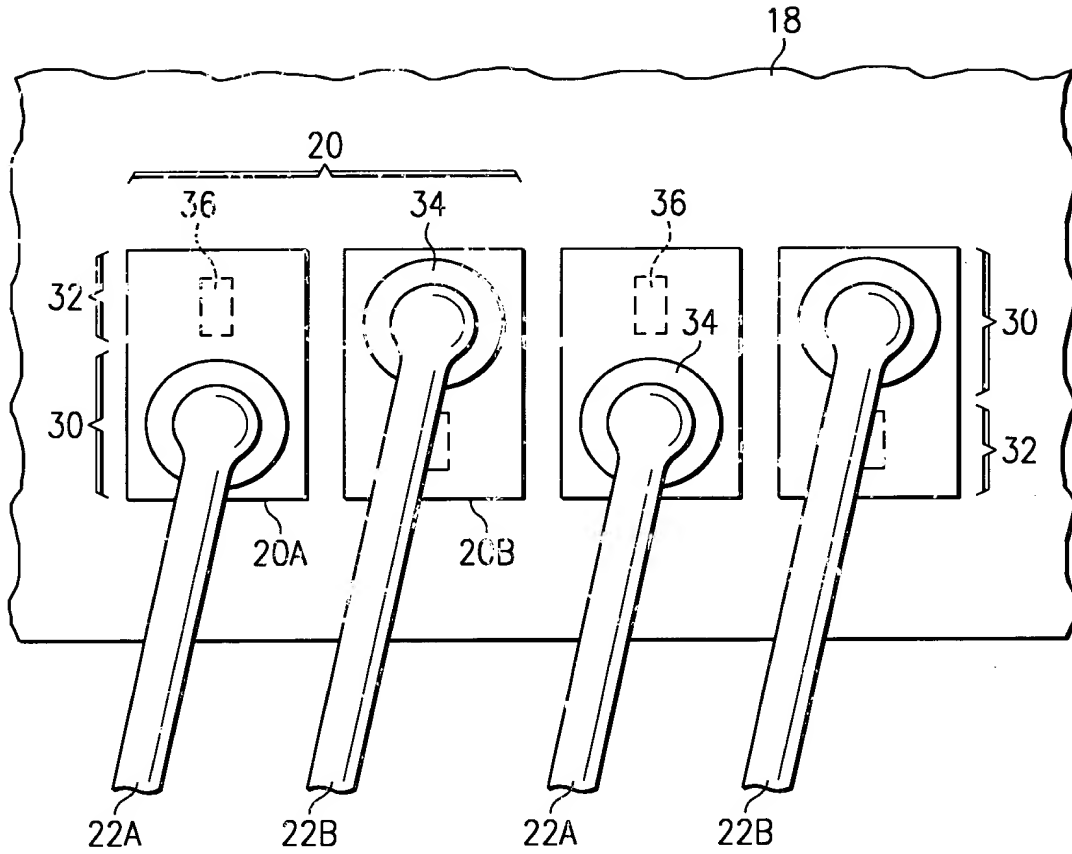


FIG. 3

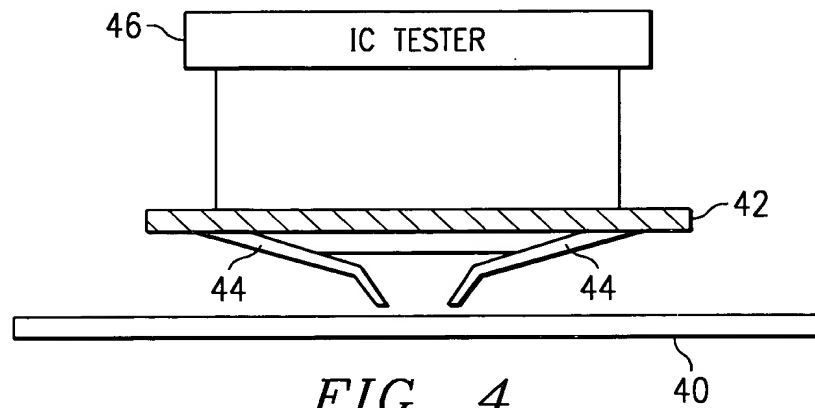


FIG. 4

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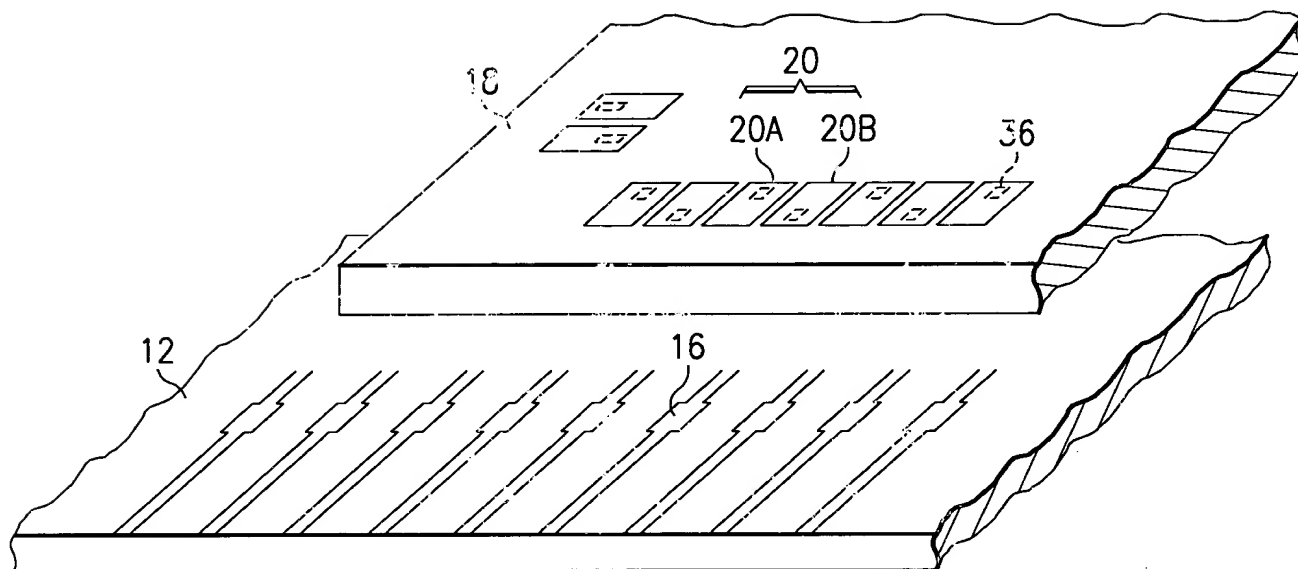


FIG. 5A

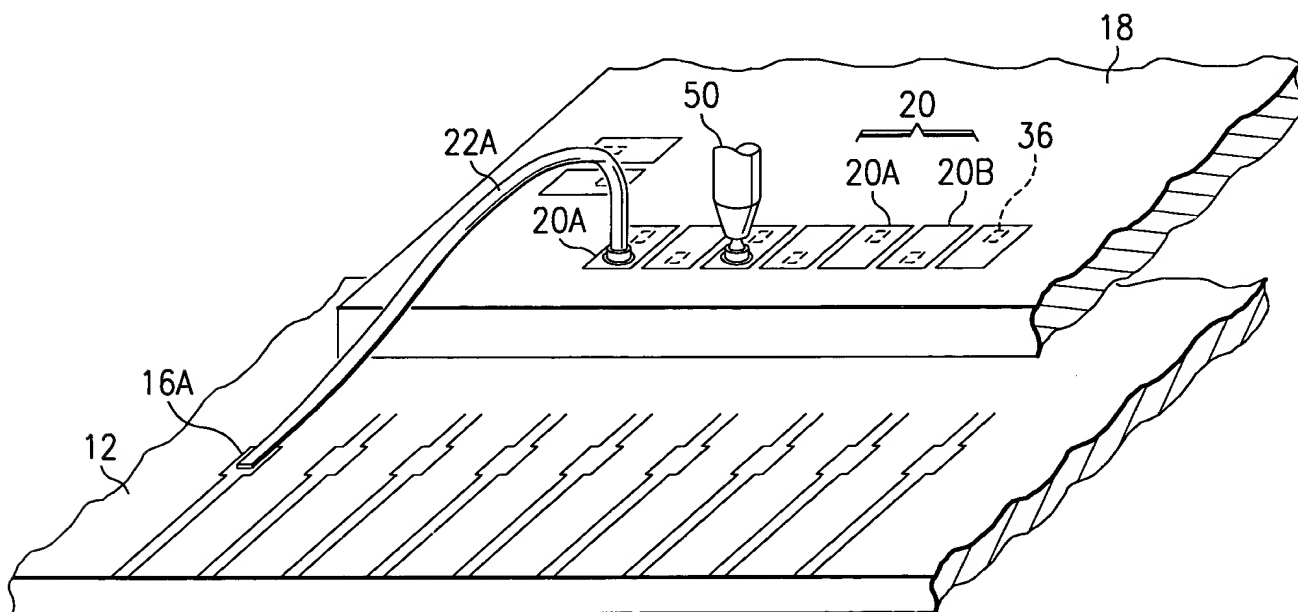


FIG. 5B



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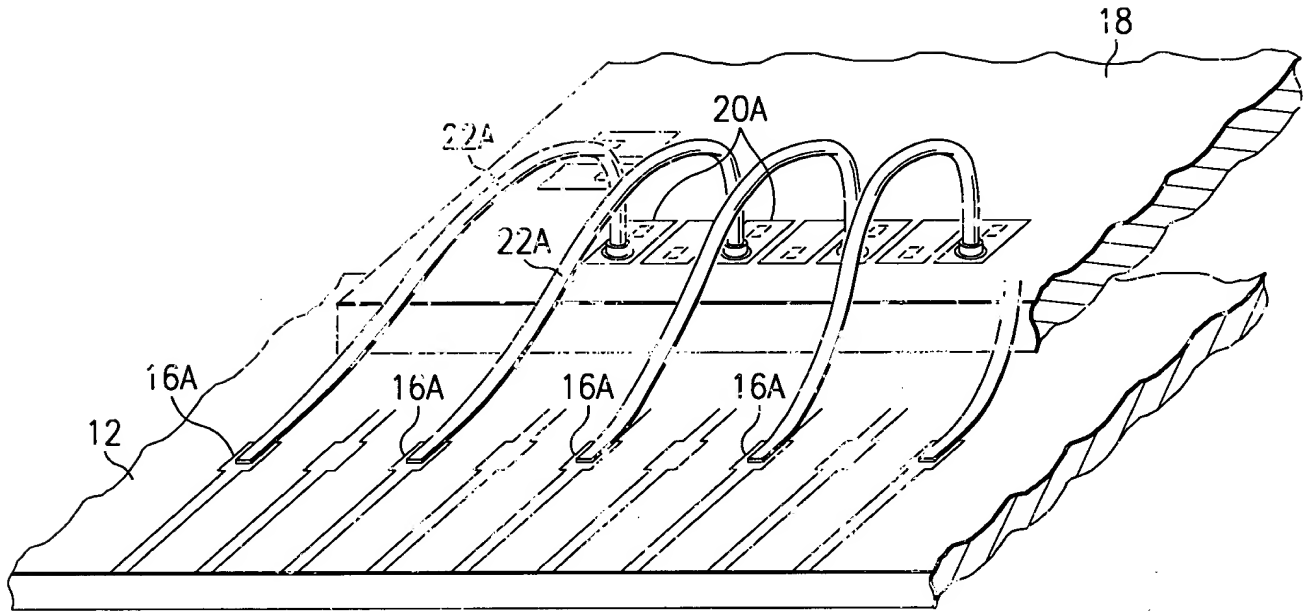


FIG. 6A

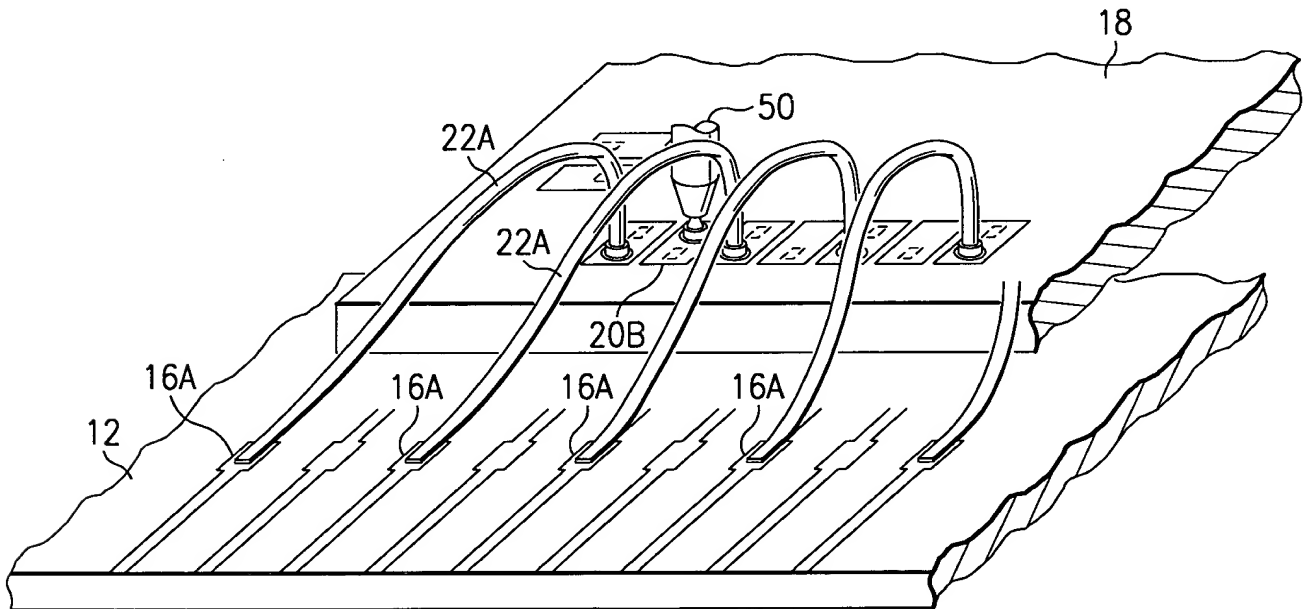


FIG. 6B

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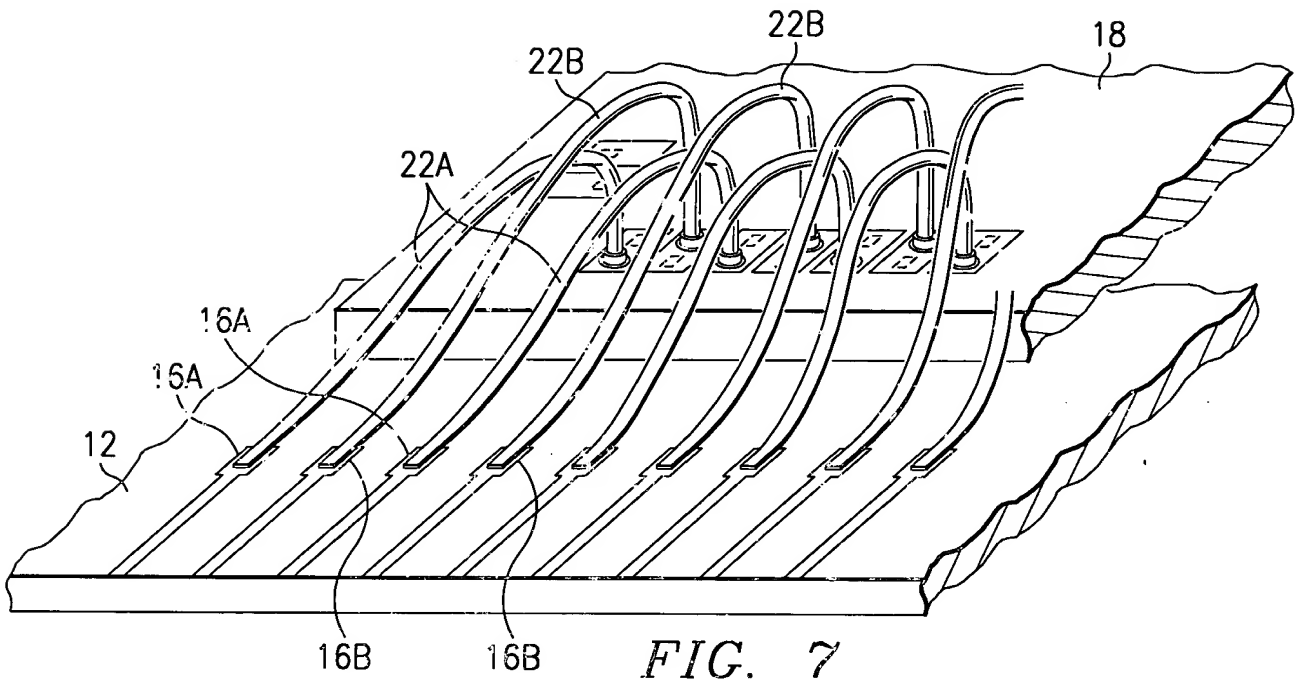


FIG. 7

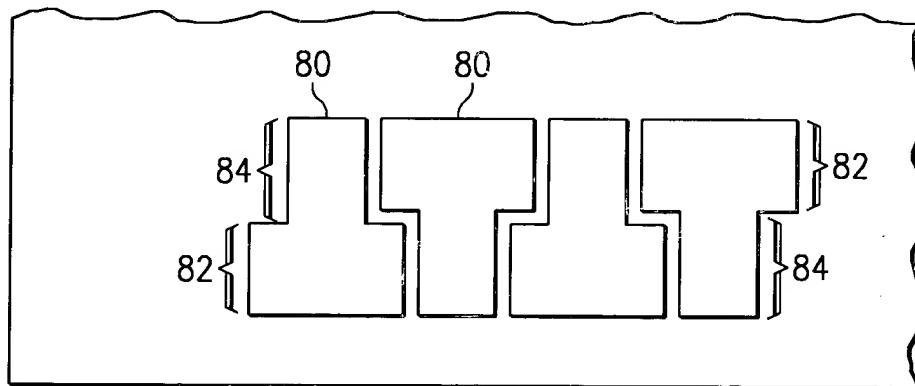


FIG. 8

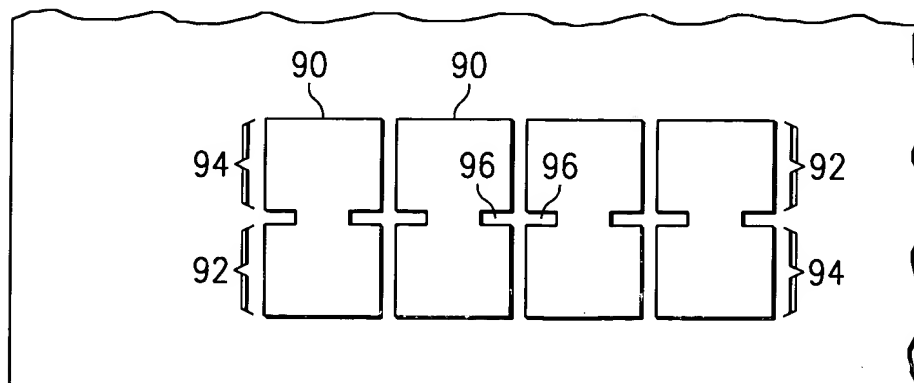


FIG. 9